

Docket No.: 069804-0015



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of	:	
Mitsutoshi FUJITA, et al.	:	Customer Number: 2077
Application No.: 10/815,690	:	Confirmation Number: 9744
Filed: April 02, 2004	:	Group Art Unit: 2891
	:	Examiner: Caridad EVERHART

For: METHOD OF DESIGNING LOW-POWER SEMICONDUCTOR INTEGRATED CIRCUIT

RESPONSE UNDER 37 C.F.R. § 1.111

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated March 2, 2006, having a three-month shortened statutory period for reply set to expire on June 2, 2006, Applicants respectfully request reconsideration of the pending rejection for the following reasons.